

FIG.1 (PRIOR ART)

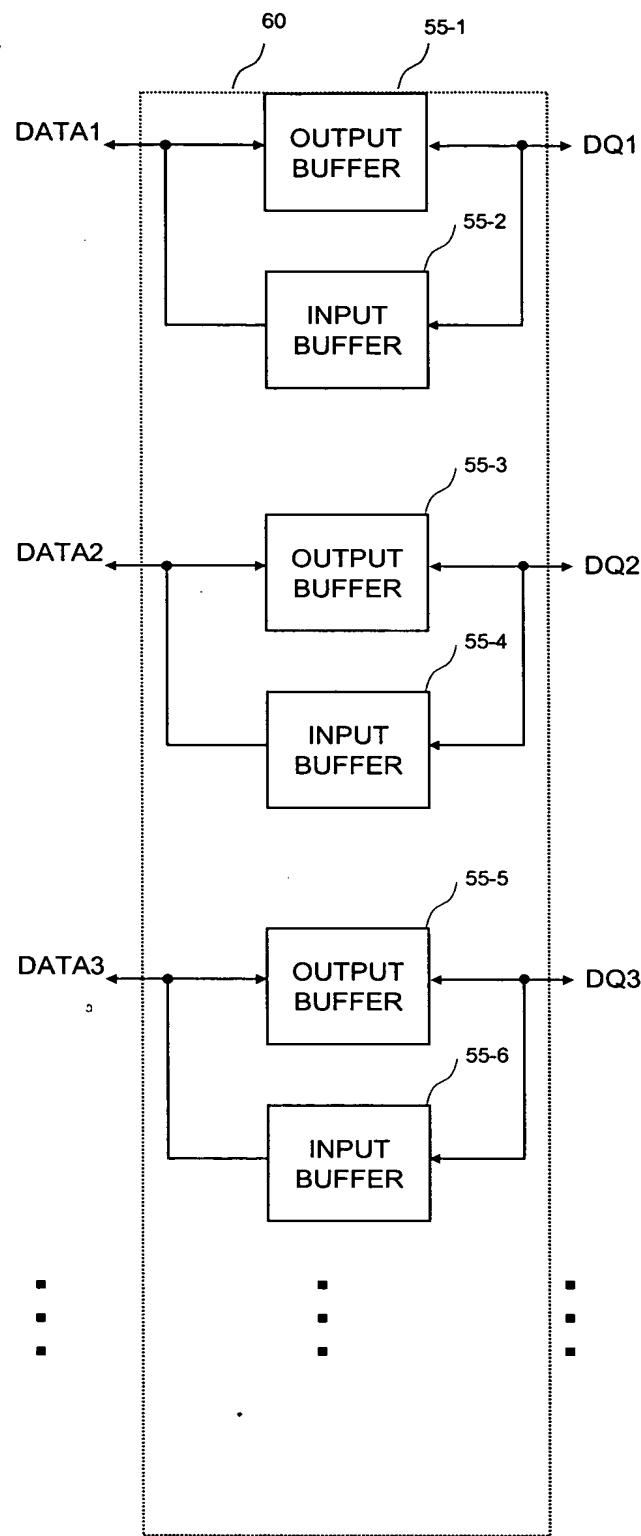


FIG.2 (PRIOR ART)

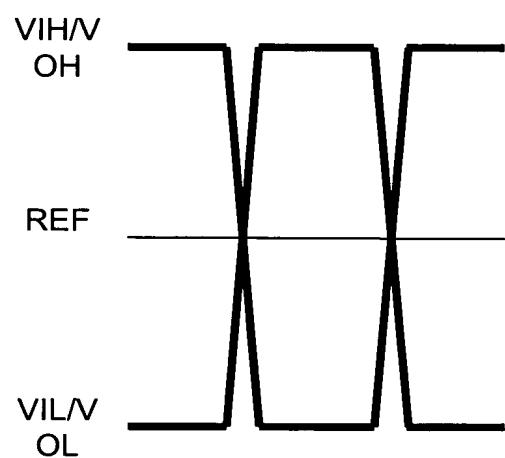


FIG.3 (PRIOR ART)

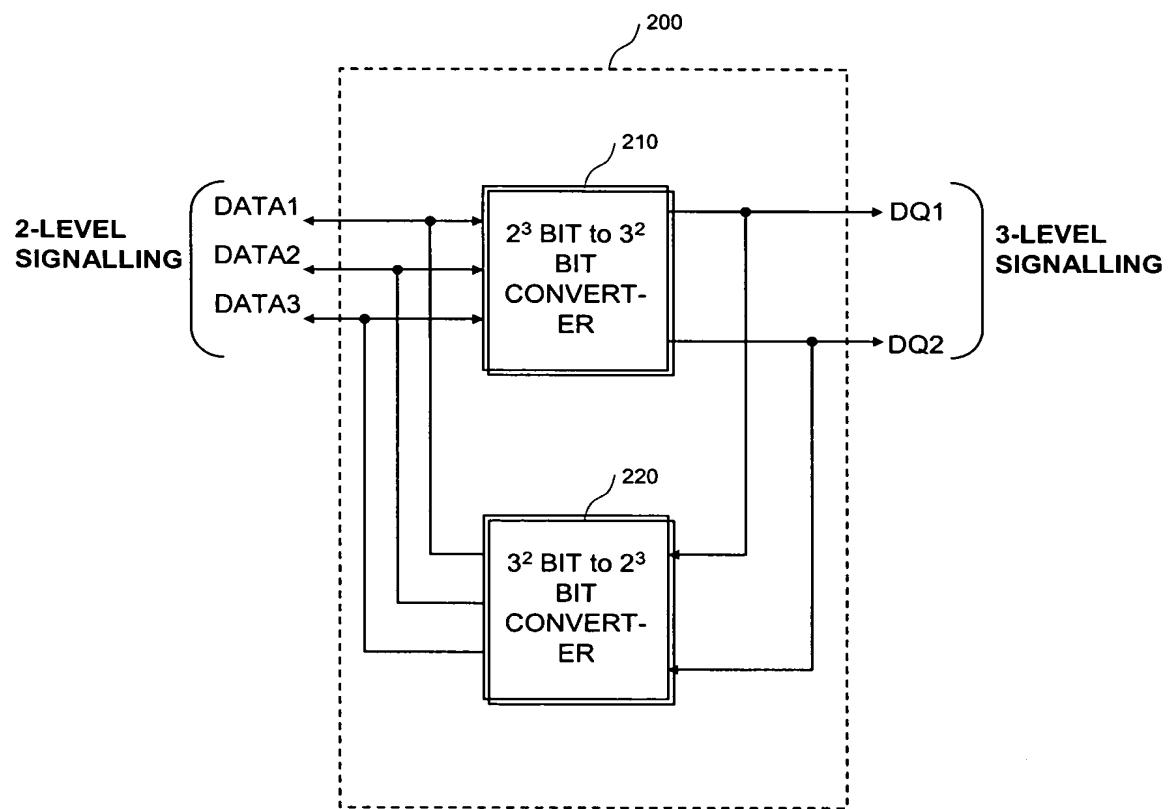


FIG.4

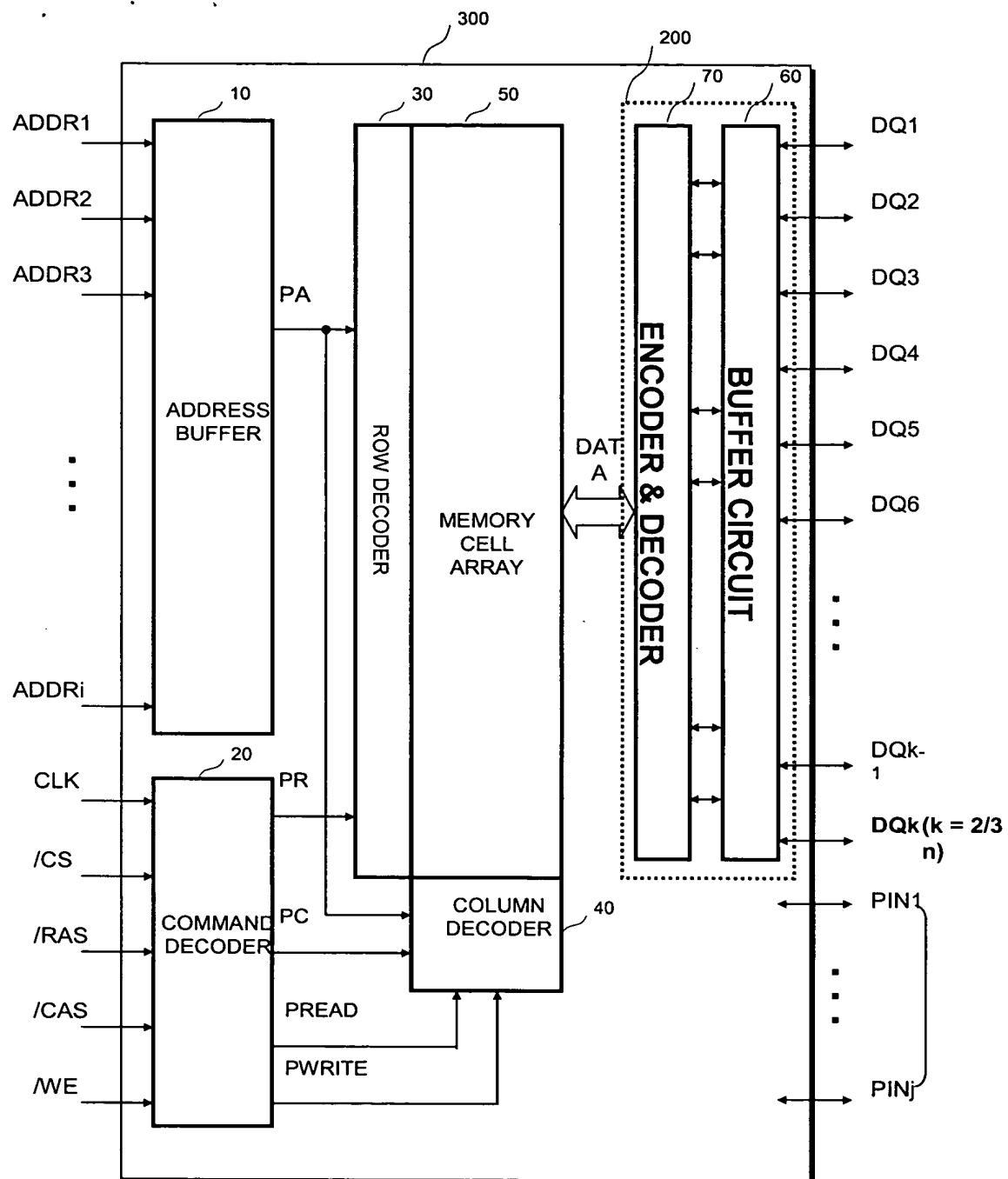


FIG.5

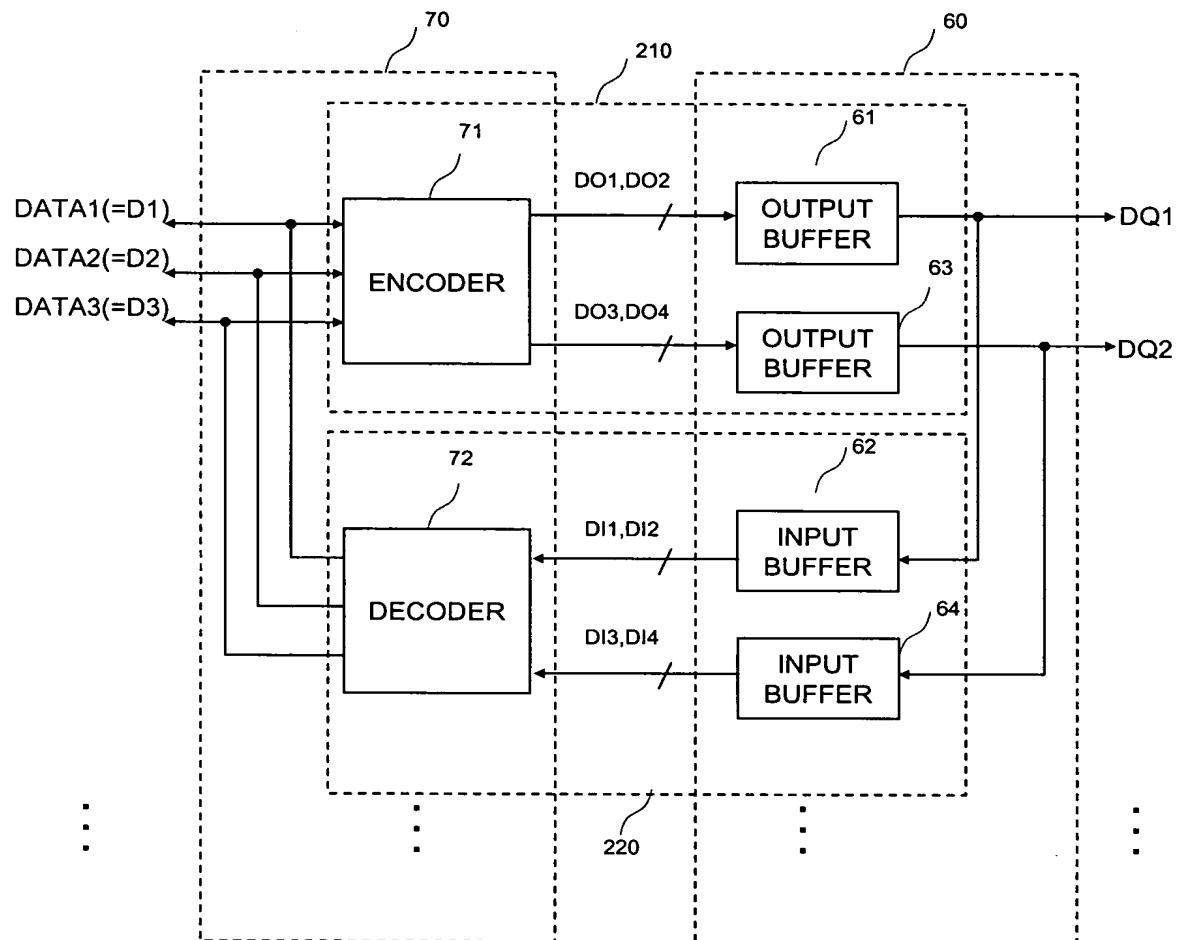


FIG.6

INPUT/OUTPUT INTERFACE OF AN INTEGRATED CIRCUIT DEVICE
Application No. [NEW] - Attorney Docket No. SEC.1095
Inventor: Seong-jin JANG

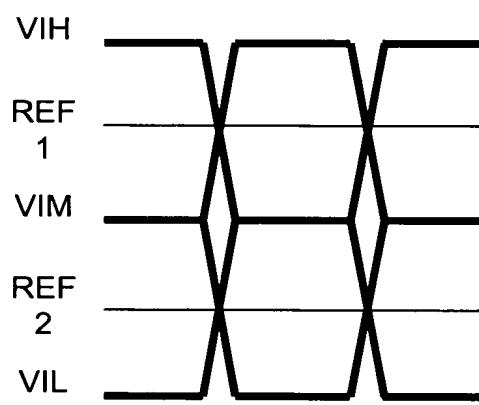


FIG.7

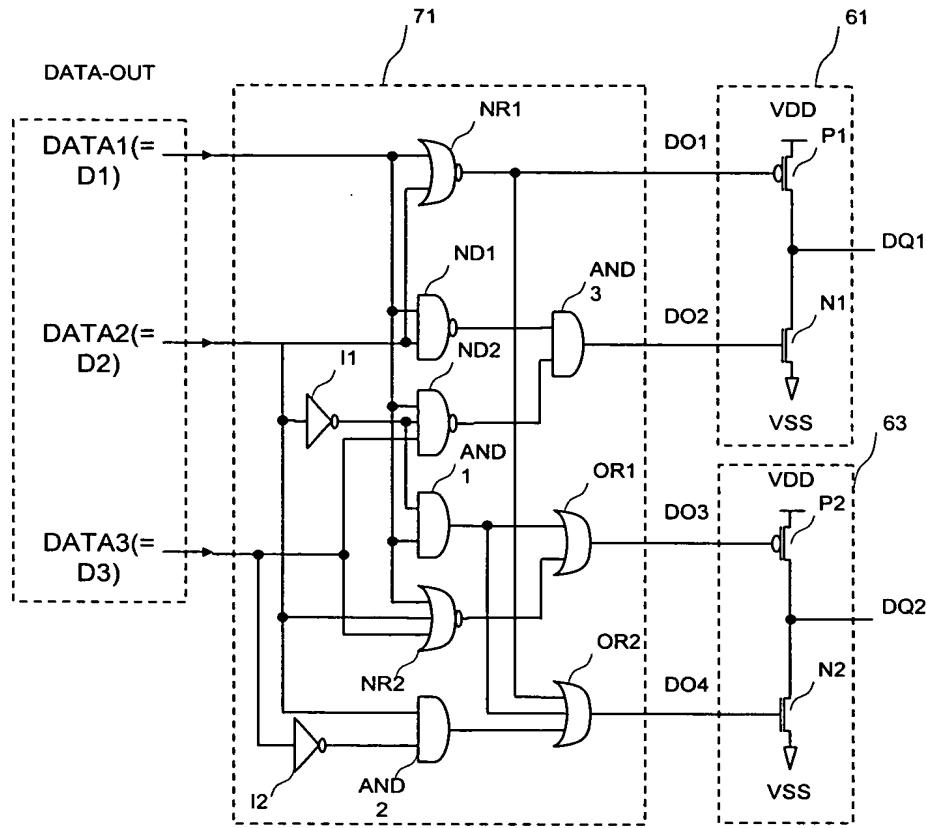


FIG.8

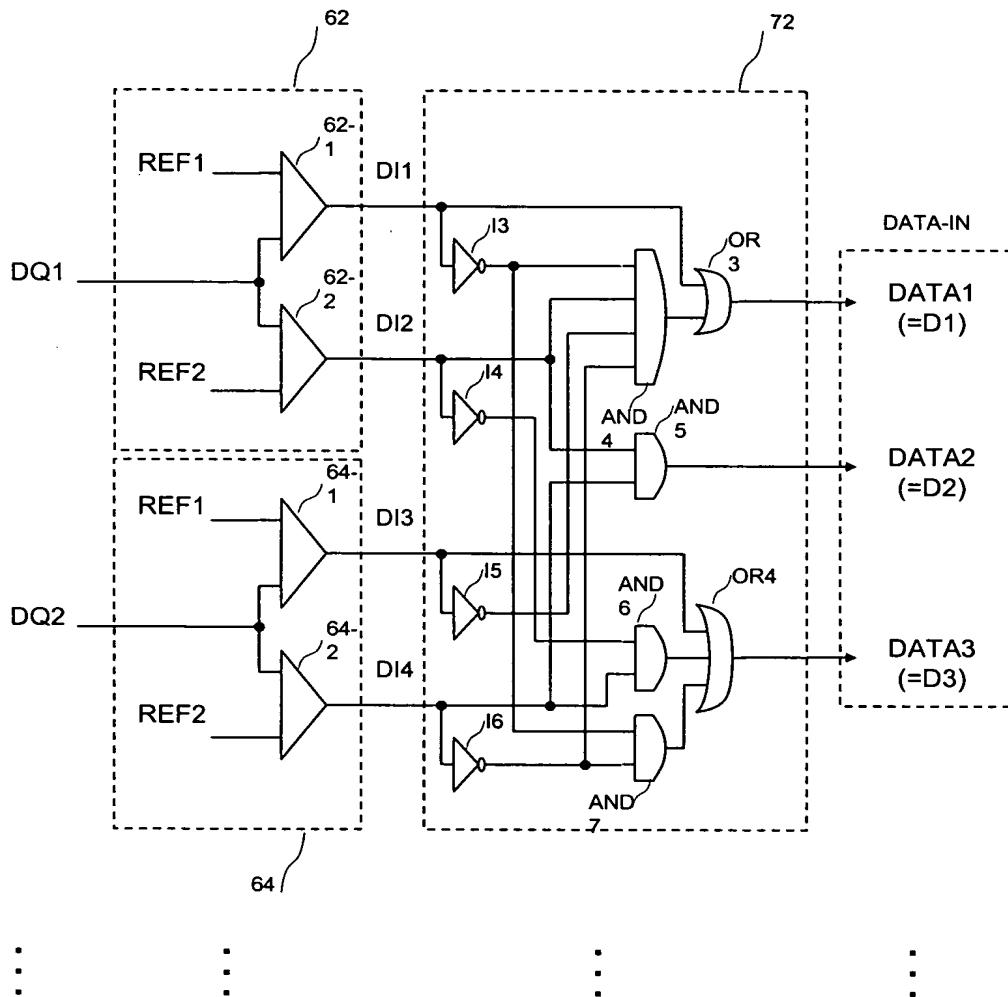


FIG.9

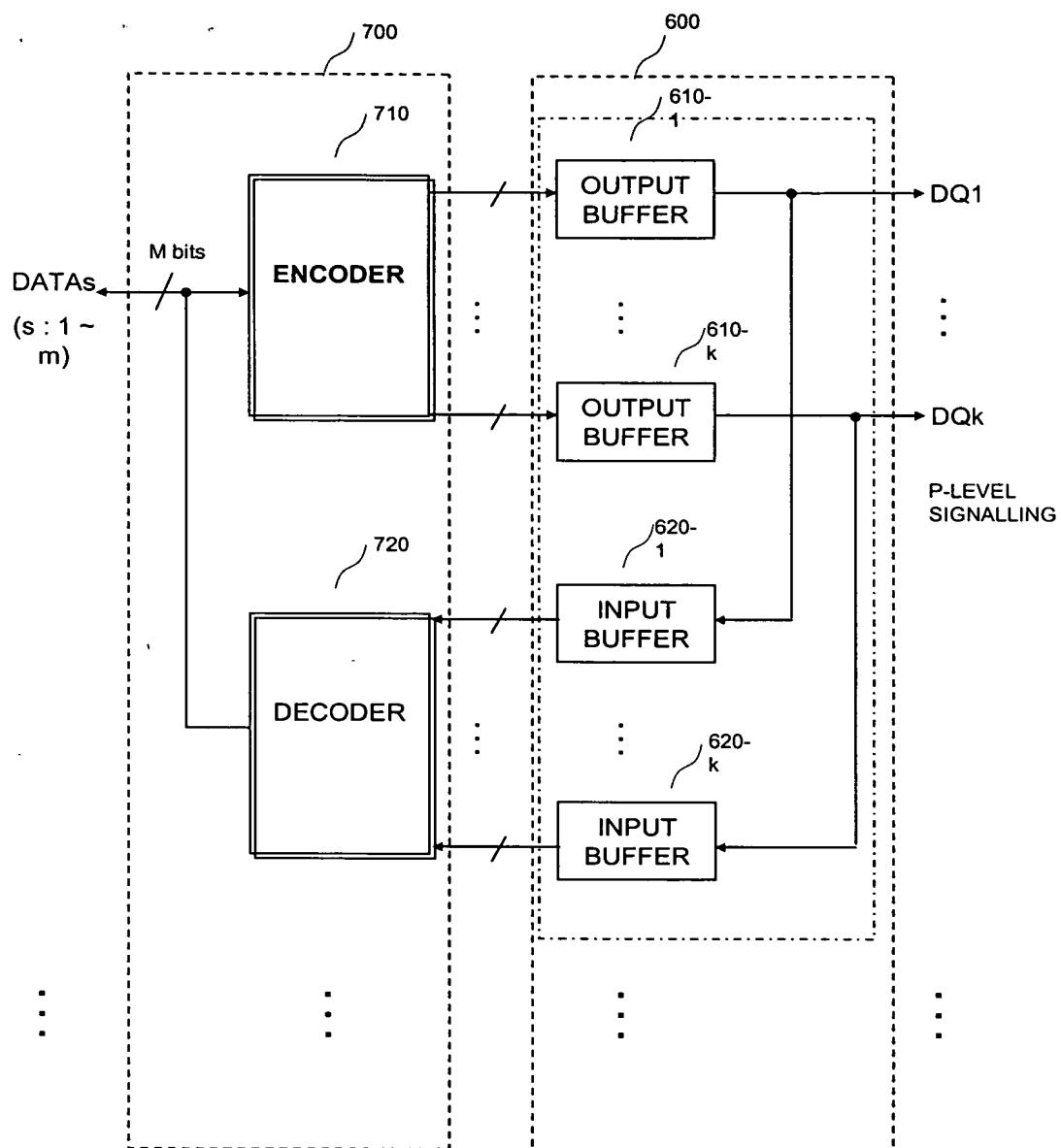


FIG.10

INPUT/OUTPUT INTERFACE OF AN INTEGRATED CIRCUIT DEVICE
Application No. [NEW] - Attorney Docket No. SEC.1095
Inventor: Seong-jin JANG

DATA-OUT			OUTPUT of ENCODER				DATA	
<i>D1</i>	<i>D2</i>	<i>D3</i>	<i>DO1</i>	<i>DO2</i>	<i>DO3</i>	<i>DO4</i>	<i>DQ1</i>	<i>DQ2</i>
0	0	0	1	1	1	1	0	0
0	0	1	1	1	0	1	0	M
0	1	0	0	1	0	1	M	M
0	1	1	0	1	0	0	M	1
1	0	0	0	1	1	1	M	0
1	0	1	0	0	1	1	1	0
1	1	0	0	0	0	1	1	M
1	1	1	0	0	0	0	1	1

FIG. 11

DATA		OUTPUT of INPUT BUFFER				DATA-IN		
DQ 1	DQ 2	DI1	DI2	DI3	DI4	D1	D2	D3
0	0	0	0	0	0	0	0	0
0	M	0	0	0	1	0	0	1
M	1	0	1	0	1	0	1	0
M	1	0	1	1	1	0	1	1
M	0	0	1	0	0	1	0	0
1	0	1	1	0	0	1	0	1
1	M	1	1	0	1	1	1	0
1	1	1	1	1	1	1	1	1

FIG. 12